Notice of Allowability	Application No. Applicant(s)		
	10/599,368	KUDO ET AL.	
	Examiner	Art Unit	
	M. Wilczewski	2822	
	IVI. VVIICZEWSKI	2022	
The MAILING DATE of this communication at All claims being allowable, PROSECUTION ON THE MERITS herewith (or previously mailed), a Notice of Allowance (PTOLNOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT of the Office or upon petition by the applicant. See 37 CFR 1.	IS (OR REMAINS) CLOSED 85) or other appropriate common RIGHTS. This application is	in this application. If not included nunication will be mailed in due cour	rse. THIS
1. $\boxtimes$ This communication is responsive to <u>the Request for Careers</u>	ontinued Examination filed on	06 October 2009.	
2. 🔀 The allowed claim(s) is/are <u>18-33</u> .			
3. Acknowledgment is made of a claim for foreign priority  a) All b) Some* c) None of the:		or (f).	
1. Certified copies of the priority documents h			
2. Certified copies of the priority documents h	• •		e 11
3. Copies of the certified copies of the priority	documents have been receive	ed in this national stage application	from the
International Bureau (PCT Rule 17.2(a)).			
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DAT noted below. Failure to timely comply will result in ABANDO THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the require	ements
4. A SUBSTITUTE OATH OR DECLARATION must be su INFORMAL PATENT APPLICATION (PTO-152) which			CE OF
5. CORRECTED DRAWINGS ( as "replacement sheets") r	must be submitted.		
(a) ☐ including changes required by the Notice of Draftsp	person's Patent Drawing Revie	w ( PTO-948) attached	
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date			
<ul><li>(b) ☐ including changes required by the attached Examir Paper No./Mail Date</li></ul>	ner's Amendment / Comment o	or in the Office action of	
Identifying indicia such as the application number (see 37 CF each sheet. Replacement sheet(s) should be labeled as such			:k) of
<ol> <li>DEPOSIT OF and/or INFORMATION about the de attached Examiner's comment regarding REQUIREMENT</li> </ol>			the
Attachment(s)	5 □ Notice of I	nformal Datont Application	
<ol> <li>Notice of References Cited (PTO-892)</li> <li>D Notice of Draftperson's Patent Drawing Review (PTO-94)</li> </ol>	<u> </u>	nformal Patent Application Summary (PTO-413),	
<ol> <li>Information Disclosure Statements (PTO/SB/08),</li> </ol>	Paper No	./Mail Date <u>20091021</u> . s Amendment/Comment	
Paper No./Mail Date  4.  Examiner's Comment Regarding Requirement for Depos			100
of Biological Material	9. ☐ Other	s Statement of Reasons for Allowar	ice
/M. Wilczewski/	<u> </u>	<u></u>	
Primary Examiner, Art Unit 2822			

## **EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Christopher Bennett on 21 October 2009.

The application has been amended as follows:

Claim 34 has been cancelled.

Claim 32 has been amended as follows:

In line 13 of claim 32 after "pattern",--; wherein the electronic component-forming conductive patterns have a coil pattern shape, and the electronic component is a coil component-- has been inserted.

The following is an examiner's statement of reasons for allowance: The closest prior art of record is the Japanese Patent JP 2004-186343 to Yamaguchi et al.

Yamaguchi et al. clearly teach an electronic component comprising conductive pattern layers and insulating layers alternately arranged with the conductive pattern layers to form a laminate, see figure 8. As shown in figure 8, in the laminate of Yamaguchi et al. no conductive patterns are disposed between one end surface of each of the insulating layers and the at least one floating dummy pattern 59. However, Yamaguchi et al. fail

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to teach or suggest the conductive patterns have a coil pattern shape and that the electronic component is a coil component. The additionally cited reference discloses electronic components which are laminates of conductive pattern layers and insulating layers and has been cited as related art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to M. Wilczewski whose telephone number is (571) 272-1849. The examiner can normally be reached on Monday-Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zandra Smith can be reached on 571-272-2429. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/M. Wilczewski/ Primary Examiner, Art Unit 2822